

Corey E. Baker

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OBJECTIVE

Seeking a tenure-track faculty position in a Electrical and Computer Engineering or Computer Science department

EDUCATION

Doctor of Philosophy in Electrical and Computer Engineering

August 2015

Masters of Science in Electrical and Computer Engineering

May 2012

Dissertation: Opportunistic Routing In Delay Tolerant Mobile Social Networks
University of Florida, Gainesville, FL

GPA: 3.50 / 4.00

Masters of Science in Electrical Engineering

June 2010

Emphasis: Control Systems, Bioelectrical, Systems
California State University Los Angeles, Los Angeles, CA

Bachelor of Science in Computer Engineering

May 2008

Minor: Mathematics
San Jose State University, San Jose, CA

SKILLS

Programming: Experienced in C & C++, Swift, Matlab, Java, Python, C#, Javascript, Objective C, VBScript, Verilog, Assembly Lang., Lisp, SQL

Hardware: Digital Design, Microprocessor Design, Computer Architecture, Embedded Systems, Biomedical Signal Processing, Digital, FPGA's

Tools and Systems: Microsoft Visual Studio, Xcode, NS3, VIM, LaTeX, Xilinx ISE, Modelsim, ClearCase, Cadence

Operating systems: Mac OSX, Linux and Unix, Windows (8, 7, Vista, 2003, XP, 2000, 9X)

WORK EXPERIENCE

1 Degree Technologies LLC, Gainesville, FL

May 2014 – Present

CEO and Co-founder

- Lead the company on designing infrastructure-free mobile ad-hoc social networks for disaster relief, emergency response, military applications, and general social networking. Created & designed the wireless protocol behind the technology.

Intel, Santa Clara, CA

October 2012 – January 2013

RF Architecture Intern

- Conducted research and implementation utilizing Bluetooth Smart along with other sensors for accurate proximity detection.

Intel, Hillsboro, OR

May 2012 – August 2012

Graduate Intern

- Part of the Intel Collaborators program. Project manager/engineer on a three person team that enabled one button touch gaming on wireless displays using C,C++, and C#. Also conducted research on Bluetooth Smart proximity detection.

Intel, Santa Clara, CA

June 2010 – August 2010

Software Engineering Graduate Intern

- Wrote code in an Intel proprietary language for Intel graphic cards to test cards performance and identify bugs in the compiler.

Hewlett Packard, Cupertino, CA

February 2007 – August 2008

Software Engineering Intern

- Wrote test automation code using VB Script for HP Itanium Servers. Scripts tested HP's Software Management Software and made sure servers were reporting accurate system information.

RESEARCH EXPERIENCE

University of Florida, Wireless and Mobile Systems Laboratory, Gainesville FL

August 2010 – Present

Research Assistant

- Conducted research under Dr. Janise McNair in Wireless Networks in the area of Cyber Physical Systems. Investigating socially aware delay tolerant networking using short-range communications such as Bluetooth LE.

University of Southern California, Brain and Body Dynamics Lab, Los Angeles CA

February 2009 – May 2010

Research Assistant

- *Thesis: Design and Implementation of a Non-Linear Dynamical System Replicating Spring Buckling Behavior.* Conducted research under Dr. Francisco Valero-Cuevas to use a linear motor in place of a spring to replicate the edge of instability using the duffing equation. The device can be used to measure the strength and dexterity of a patient's hand and how much vision factors in when dexterity deteriorates.

LEADERSHIP AND ACTIVITIES

Kappa Alpha Psi Fraternity, INC

April 2009 - Present

Member

National Society of Black Engineers

National Treasurer Emeritus (Board of Director position)

May 2014 – April 2015

National Treasurer (Board of Director position)

May 2012 – April 2014

Region Six Chairperson Emeritus and Parliamentarian

May 2009 – April 2010

Region Six Chairperson (Board of Director position)

May 2008 – April 2009

Region Six Treasurer

May 2007 – April 2008

HONORS & ACHIEVEMENTS

- University of California Presidents Postdoctoral Fellow (2015–present)
- NSF SEAGEP Scholarship (2011 and 2013)
- McKnight Dissertation Fellow (2014–2015)
- NSF LSAMP Bridges to the Doctorate Fellow (2008–2010)
- The National GEM Consortium PhD Fellow (2010–2015)
- Hewlett Packard Scholar (2004–2008)
- Intel Scholar (2010–2015)

